

Fig. 1

Description of Operation - Analog Integration Implementation

Timing for diagram 1:

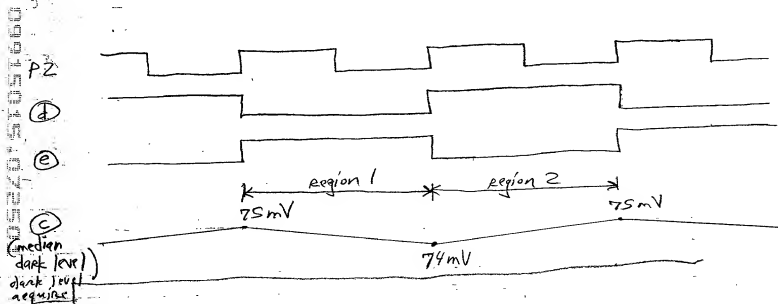
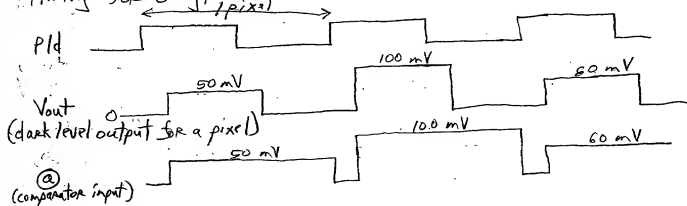


Fig. 2

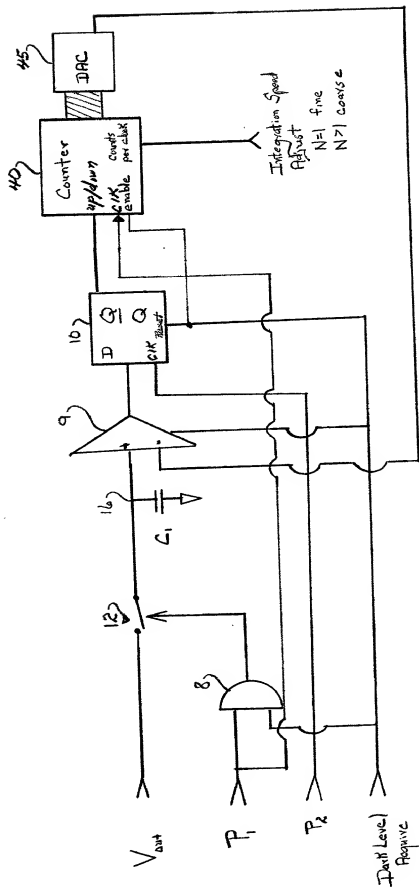


Fig. 3